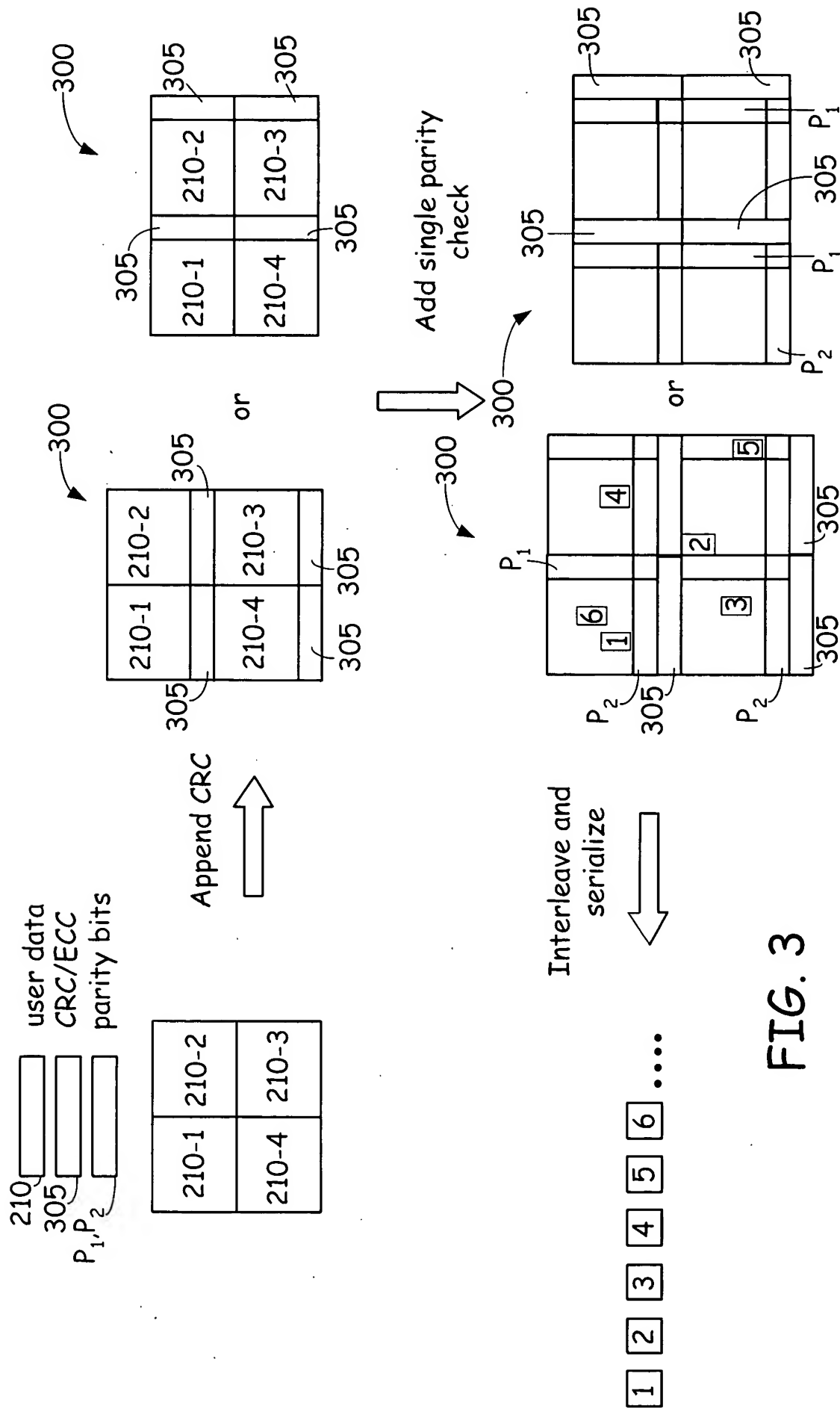


FIG. 2



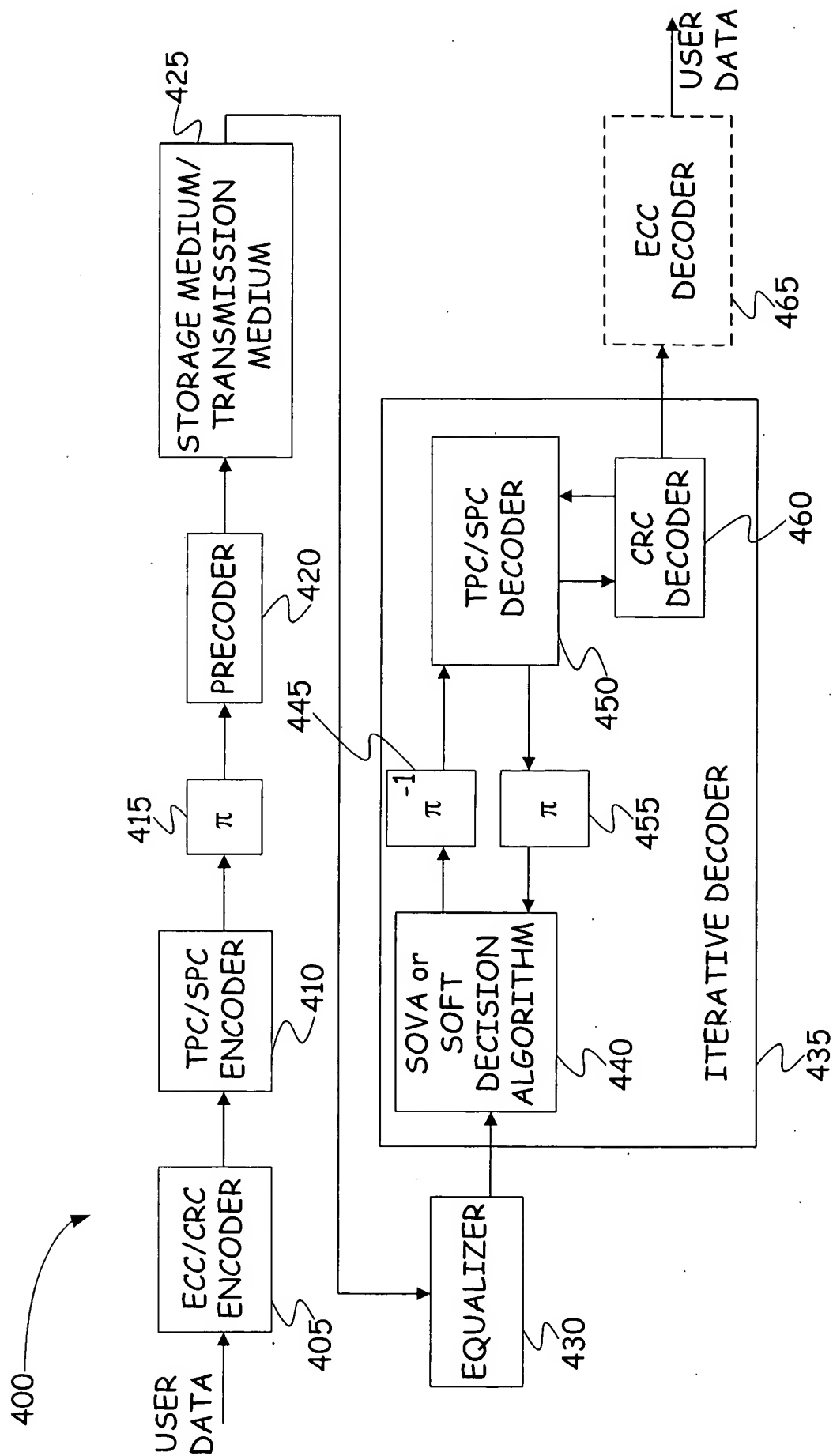
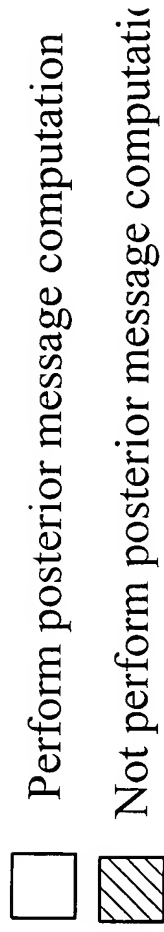
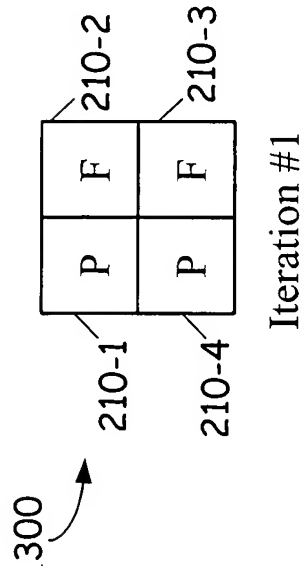


FIG. 4



No sub-block CRC check:



With CRC sub-block check:

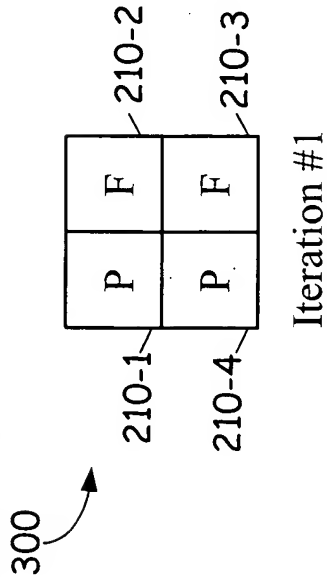
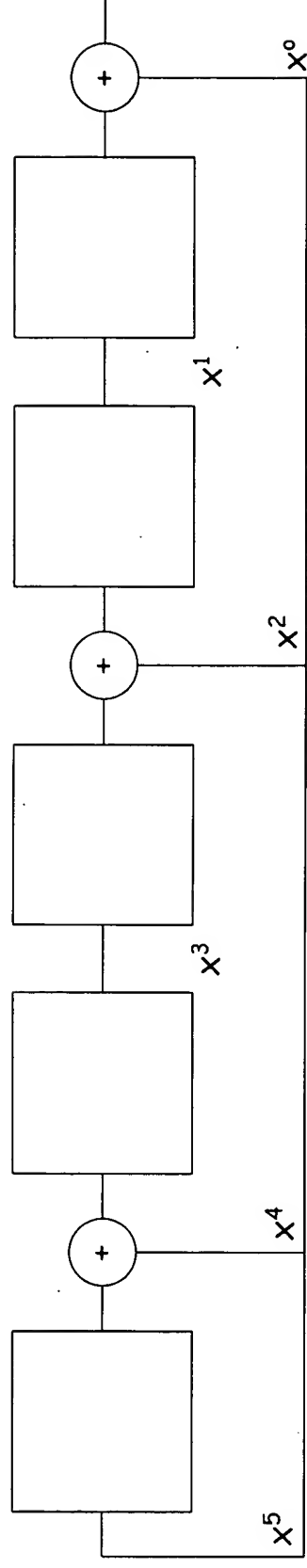


FIG. 5

Input bits when creating CRC 1010001101000000

Input bits when creating CRC 1010001101011110

460



$$P(x) = x^5 + x^4 + x^2 + 1$$

FIG. 6

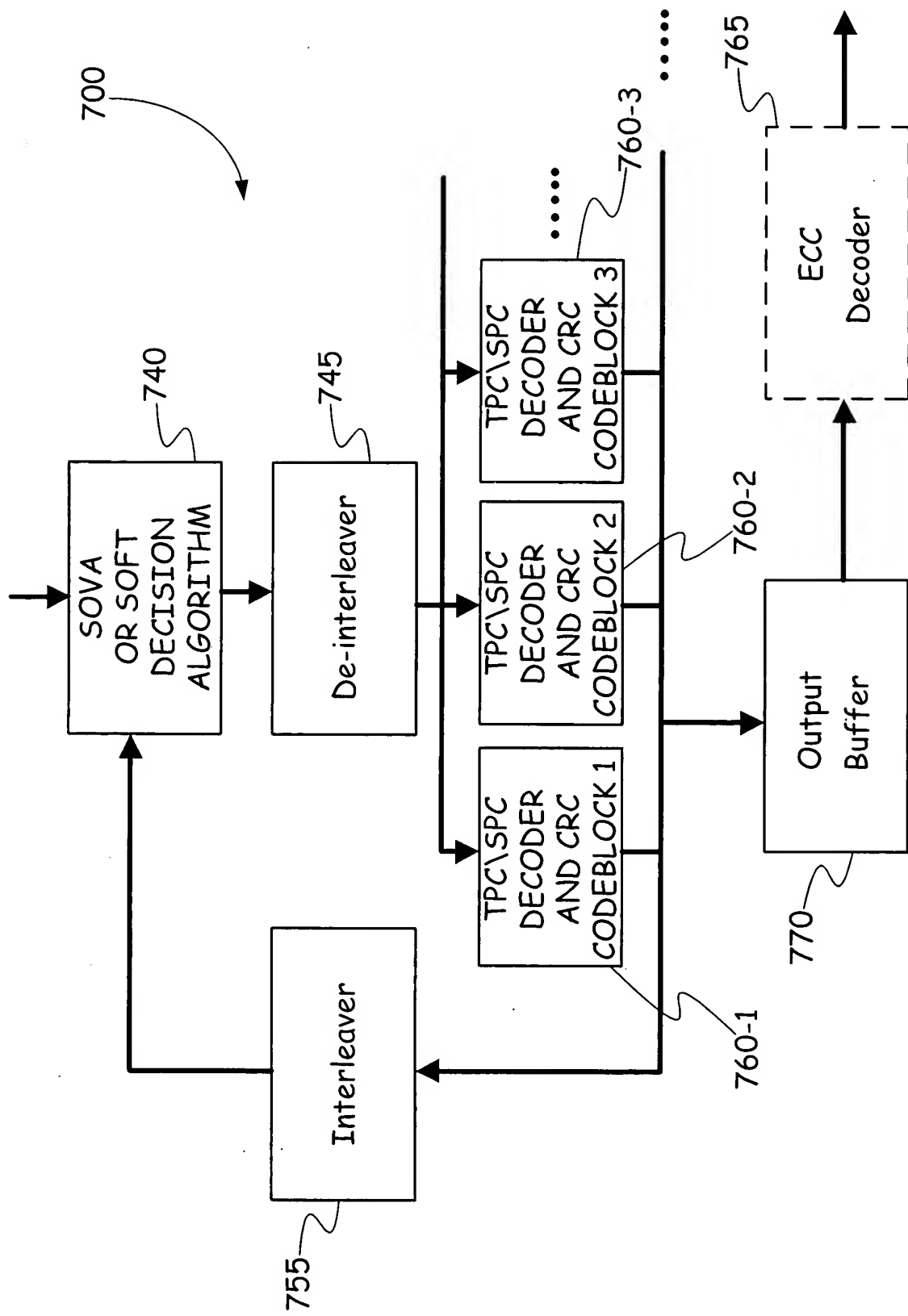


FIG. 7

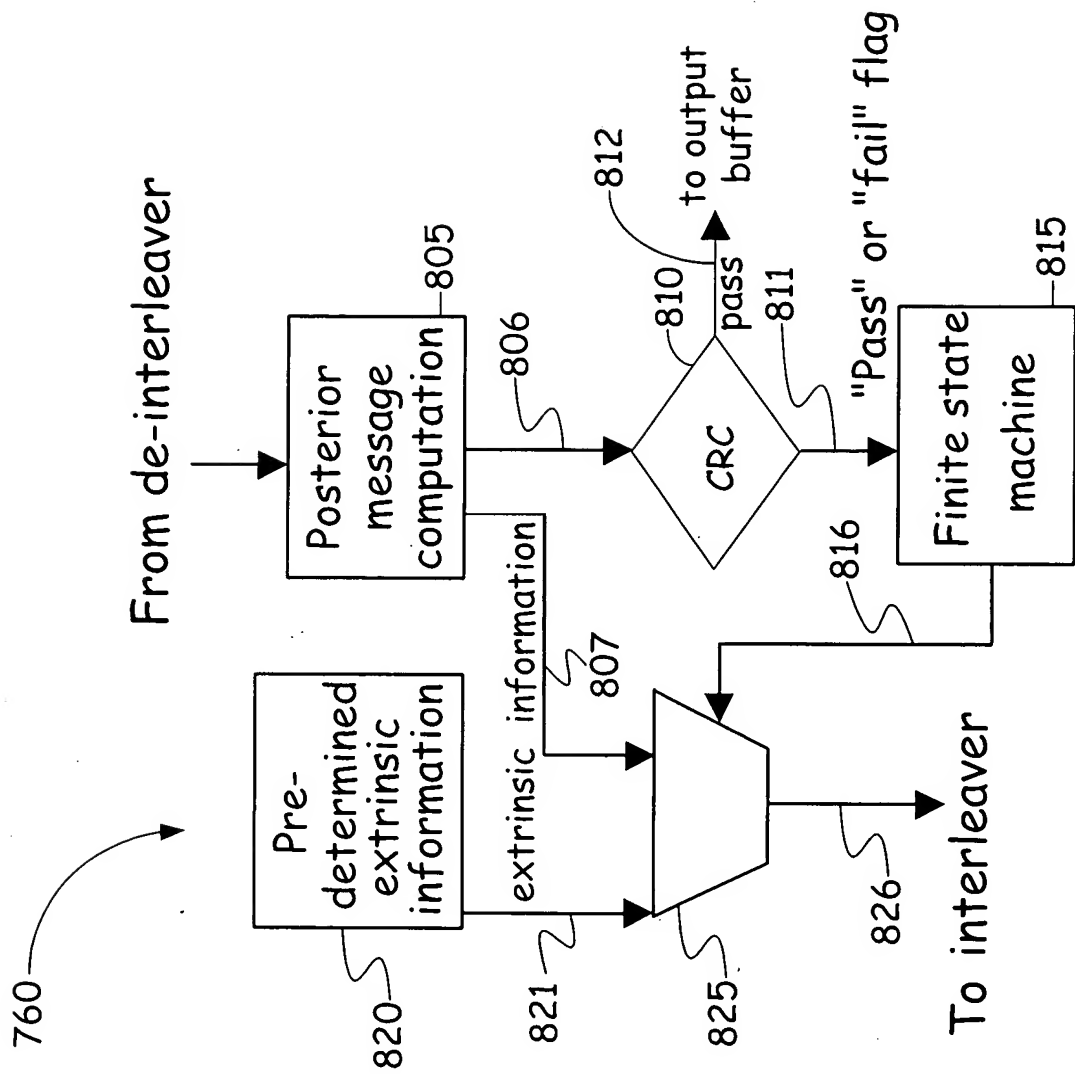
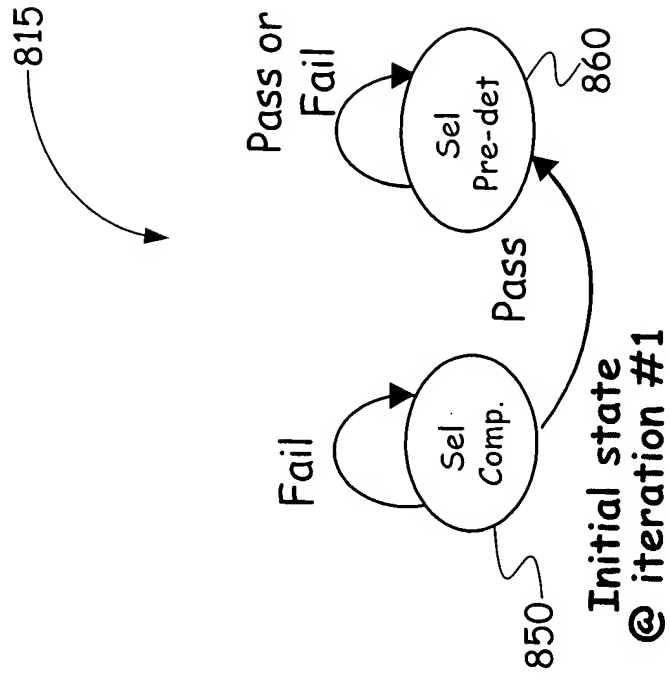
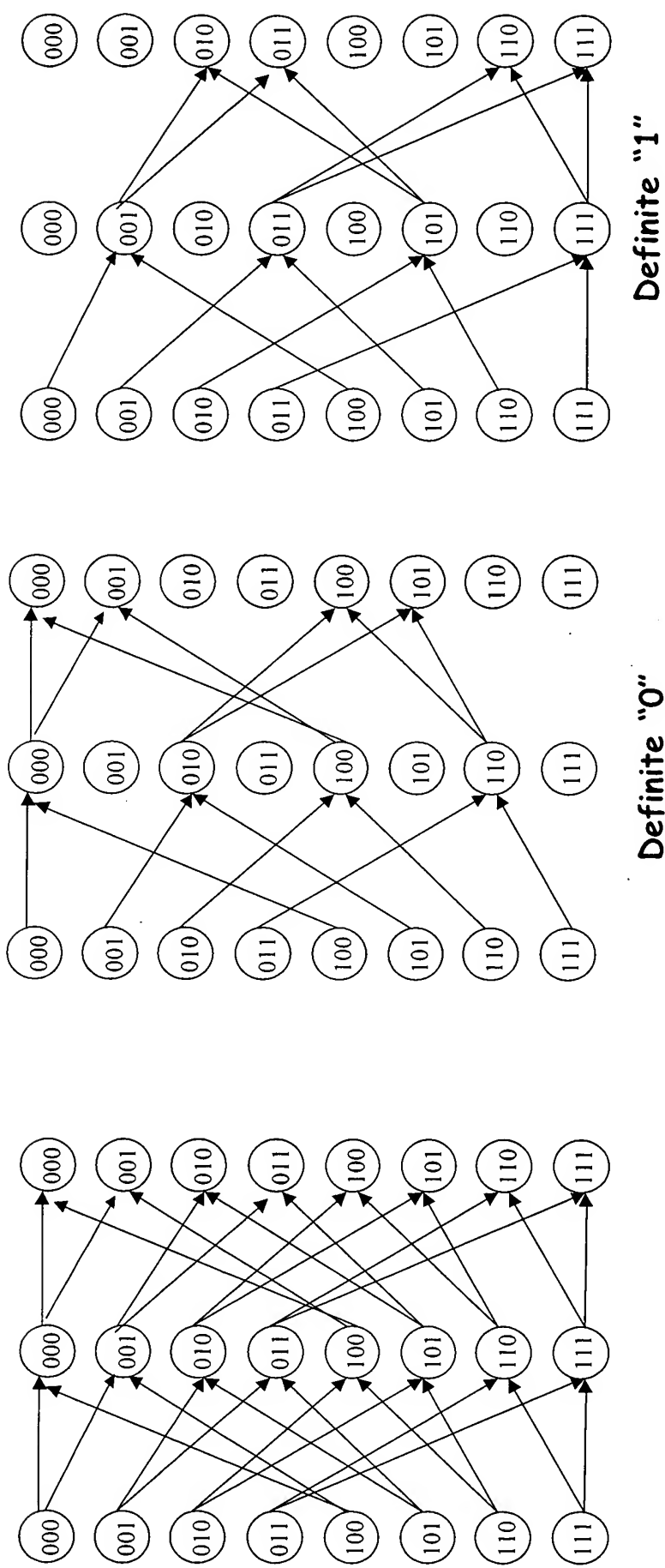


FIG. 8-1



Finite state
machine

FIG. 8-2



Normal trellis

Definite "0"

Definite "1"

Trellis with the second column to have a definite value

FIG. 9